

CLAIMS

1. An individual chip with one or more protective polymeric wafer scale caps, made by a method, the method including:

5 a) forming, in a two part mold, an array of first hollow molded caps, from a layer of thermoplastic material which is placed in a mold, the mold having first and second mold halves which are brought together to form the caps, each cap having a central portion and a perimeter wall;

b) opening the mold so that the caps are carried by the first half;

c) applying, using the first half, the first caps to one side of a wafer; then

10 d) separating the wafer into individual chips.

2. The chip made by the method of claim 1 wherein, the mold and the wafer are made from the same material.

15 3. The chip made by the method of claim 1 wherein, the mold is made from a semiconductor.

4. The chip made by the method of claim 1 wherein, the wafer is separated into individual packages by a etching of the wafer for which the first caps are a mask.

20 5. The chip made by the method of claim 1 wherein, applying the caps further comprises using a cooperating release wafer having eject pins to urge a cap into position.

6. The chip made by the method of claim 5 wherein, the first mold half has openings for receiving the pins, the pins being longer than the openings.

7. The chip made by the method of claim 5 wherein, there is a gap between the first mold half and the release wafer and using the release wafer further comprises applying a vacuum to the gap to draw the release wafer toward the first mold half.

30 8. The chip made by the method of claim 1 wherein, the cap is formed from a layer of thermoplastic material.

9. The chip made by the method of claim 8 wherein, forming the cap comprises heating the thermoplastic material with infrared radiation, the mold being essentially transparent to the infrared radiation.

5 10. The chip made by the method of claim 1 wherein, the first and second mold halves each have interior surfaces penetrated by holes for receiving pins which are formed on adjacent release wafers, the pins being longer than the holes but being flush with the interior surfaces during the forming .

10 11. The chip made by the method of claim 1 wherein, the thermoplastic material is 200-500 microns thick prior to being formed into caps.

12. The chip made by the method of claim 1 wherein, when the mold is closed, there is a thin layer of the material from which the caps are formed joining the caps into an array
15 and the thin layer is removed by a squeezing action of the mold.

13. The chip made by the method of claim 1 wherein, when the mold is opened, there is a thin layer of the material from which the caps are formed joining the caps into an array
20 and the thin layer is removed by an etching process.

14. The chip made by the method of claim 13 wherein, the thin layer is removed by an oxygen plasma etching.

15. The chip made by the method of claim 12, wherein, the first half has first portions
25 which separate adjacent areas; the second half has second portions which separate adjacent grooves; the first and second portions coming together when the halves are brought together such that material is squeezed out from between the first and second portions, separating adjacent caps.

30 16. The chip made by the method of claim 1, wherein, the first mold half has a lower surface into which is formed a series of recesses defined by lateral edges, the second mold half having an upper surface in which is formed a series of grooves, the grooves aligning

with the edges, the recesses and grooves defining cavities having a repeat spacing that corresponds to a spacing on a wafer to which the caps will be applied.

17. The chip made by the method of claim 15, wherein the first and second portions of the molds have not been etched.

18. The chip made by the method of claim 1, wherein:
the first half includes first holes formed through it;
there being provided a first half release wafer from which project pins;
the pins located in registry with the first holes;
the first half having a thickness in the area of the first holes, the pins being longer than the thickness;
the first half release wafer having a first position in which the pins are flush with interior ends of the first holes;
there being a gap between the first half and the first half release wafer when the first half release wafer is in the first position; and wherein
a vacuum is applied to the gap to eject the caps.

19. The chip made by the method of claim 18, wherein:
the second half includes second holes formed through it;
there being provided a second half release wafer from which project pins;
the pins located in registry with the second holes;
the second half having a thickness in the area of the second holes, the pins being longer than the thickness;
the second half release wafer having a first position in which the pins are flush with an interior end of the second holes;
there being a second gap between the second half and the second half release wafer when the second half release wafer is in the first position.